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• There are 3 questions. The points for each question are given in the square brackets, next to the question title. The overall maximum score is 100. This final exam weighs 40% of your final grade.

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Answers! 1. (9 pts)

Complete the following table of equivalent values. Binary

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1011.0011 13.14 11.1875 B.3

11101.11111101 35.77 29.99

1D.FD 11011.010011 33.23 27

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353 - Digital Circuits Final
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- 11:00 AM Auxiliary

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Please review the

examination rules on Page 2.

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Evaluation Strategy : Your final grade in this course will be based on seven quizzes (50% total), one mid-term exam (20%) and a

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Comprehensive final exam (30%). We'll drop 2 quizzes with the lowest score. No alternative test arrangements can be made. Graded quizzes and exams will be returned through the distribution center.

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CS 151 S008 Digital Logic Design

pass the exam. requires at least . 11 points. from A1 + A2 , if fewer points we will not look at the rest of your exam. Part B (Design

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Answers) contains two design problems of a total of 10 points. Part B is . corrected only if there are at . least 11p. from the exam A- Part. NOTE !

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ECE-2 78 : Digital Logic Design Fall 2016. Solutions - Midterm Exam (October 13th @ 5:30 pm) Presentation and clarity are very important! Show your

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Answers! PROBLEM 1 (20 PTS) a) Complete the following table. The decimal numbers are unsigned: (6 pts.)

Decimal	BCD	Binary	Reflective Gray Code
52	01010010	110100	101110.
34	00110100	100010	110011

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203-Digital Systems
DESIGN (Fall 2015)

MEF University Assigned:

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12:00am on January 4, 2016.

Due: 1:50pm on January 4,
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University Instructor:

S, uayb S, . Arslan. Name:

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