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Q. 4.1: Consider the combinational circuit shown in Fig. P4.1.(a)* Derive the Boolean expressions fo

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Digital circuit is a connection of two or more logic gates Digital network can be described using schematic diagrams, Boolean expressions, or truth tables $x y z$

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a) $(BEE)r = (2699)_{10} 11 \times r^2 + 14 \times r^1 + 14 \times r^0 = 2699 11 \times r^2 + 14 \times r - 2685 = 0$. By the quadratic equation: $r = 15$ or $r \approx -16.27$ ANSWER: $r = 15$ b) $(365)r = (194)_{10} 3 \times r^2 + 6 \dots$

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Solution: Inputs: 8-bits (A = 4-bits , B = 4-bits) A and B are two 4-bit numbers 4-bit input 4-bit magnitude GT Let A = A₃A₂A₁A₀ , and A comparator Let B = B₃B₂B₁B₀ EQ 4-bit input Inputs have 28 (256) possible B combinations (size of truth table LE

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